

L Numb r	Hits	S arch T xt	DB	Tim stamp
-	7338	pixel ADJ electr de	USPAT; EPO; JPO	2001/07/02 20:55
-	22931	thin ADJ film ADJ transistor r TFT	USPAT; EPO; JPO	2001/07/02 20:56
-	48082	storage ADJ capacit\$4	USPAT; EPO; JPO	2001/07/02 20:57
-	1135	metallic ADJ pattern	USPAT; EPO; JPO	2001/07/02 20:58
-	4557	(pixel ADJ electrode) and (thin ADJ film ADJ transistor or TFT)	USPAT; EPO; JPO	2001/07/02 20:58
-	699	(storage ADJ capacit\$4) and ((pixel ADJ electrode) and (thin ADJ film ADJ transistor or TFT))	USPAT; EPO; JPO	2001/07/02 20:58
-	0	(metallic ADJ pattern) and ((storage ADJ capacit\$4) and ((pixel ADJ electrode) and (thin ADJ film ADJ transistor or TFT)))	USPAT; EPO; JPO	2001/07/02 20:59
-	17508	wiring ADJ pattern	USPAT; EPO; JPO	2001/07/02 21:00
-	28	(wiring ADJ pattern) and ((storage ADJ capacit\$4) and ((pixel ADJ electrode) and (thin ADJ film ADJ transistor or TFT)))	USPAT; EPO; JPO	2001/07/05 12:04
-	4	electrode ADJ pattern	USPAT; EPO; JPO	2001/07/02 21:03
-	9840	electrode ADJ pattern	USPAT; EPO; JPO	2001/07/02 21:04
-	50	drain ADJ electrode ADJ pattern	USPAT; EPO; JPO	2001/07/02 21:05
-	50	storage ADJ electrode ADJ pattern	USPAT; EPO; JPO	2001/07/05 12:08
-	0	(drain ADJ electrode ADJ pattern) and (storage ADJ electrode ADJ pattern)	USPAT; EPO; JPO	2001/07/02 21:05
-	100	(drain ADJ electrode ADJ pattern) or (storage ADJ electrode ADJ pattern)	USPAT; EPO; JPO	2001/07/02 21:06
-	27943	contact ADJ hole	USPAT; EPO; JPO	2001/07/02 21:07
-	5	(drain ADJ electrode ADJ pattern) and (contact ADJ hole)	USPAT; EPO; JPO	2001/07/02 21:12
-	1	("5708483").PN.	USPAT; EPO; JPO	2001/07/02 21:13
-	2	("5737049").PN.	USPAT; EPO; JPO	2001/07/02 21:14
-	2	("5435324").PN.	USPAT; EPO; JPO	2001/07/02 21:14
-	41	(storage ADJ electrode ADJ pattern) and (contact ADJ hole)	USPAT; EPO; JPO	2001/07/05 12:19
-	0	storage ADJ electrode ADJ pattern with (periph ry r annular)	USPAT; EP ; JPO	2001/07/03 08:13
-	50	storage ADJ electrod ADJ pattern	USPAT; EPO; JPO	2001/07/03 08:16
-	41	(st rage ADJ lectrode ADJ pattern) and (c ntact ADJ hol)	USPAT; EPO; JPO	2001/07/03 13:07

-	0	((storage ADJ electrode ADJ pattern) and (contact ADJ hole)) and pixel	USPAT; EPO; JPO	2001/07/03 08:19
-	0	(storage ADJ electrode ADJ pattern) and pixel	USPAT; EPO; JPO	2001/07/03 08:19
-	0	(storage ADJ electrode ADJ pattern) and TFT	USPAT; EPO; JPO	2001/07/03 08:20
-	618	(electrode ADJ pattern) and (contact ADJ hole)	USPAT; EPO; JPO	2001/07/03 08:22
-	138	((electrode ADJ pattern) and (contact ADJ hole)) and pixel	USPAT; EPO; JPO	2001/07/03 08:22
-	93	((electrode ADJ pattern) and (contact ADJ hole)) and pixel) and TFT	USPAT; EPO; JPO	2001/07/03 08:22
-	3	((electrode ADJ pattern) and (contact ADJ hole)) and pixel) and TFT) and storage ADJ electrode	USPAT; EPO; JPO	2001/07/03 16:34
-	1	((electrode ADJ pattern) and (contact ADJ hole)) and pixel) and TFT) and drain ADJ electrode ADJ pattern	USPAT; EPO; JPO	2001/07/03 13:05
-	64	((electrode ADJ pattern) and (contact ADJ hole)) and pixel) and TFT) and drain ADJ electrode	USPAT; EPO; JPO	2001/07/03 08:58
-	0	((electrode ADJ pattern) and (contact ADJ hole)) and pixel) and TFT) and drain ADJ electrode) and (peripheral or annular) ADJ pattern	USPAT; EPO; JPO	2001/07/03 08:40
-	7	(peripheral or annular) ADJ storage ADJ electrode	USPAT; EPO; JPO	2001/07/03 09:01
-	15	(peripheral or annular) ADJ drain ADJ electrode	USPAT; EPO; JPO	2001/07/03 09:58
-	28	((electrode ADJ pattern) and (contact ADJ hole)) and pixel) and TFT) and (driving with peripheral or annular)	USPAT; EPO; JPO	2001/07/03 13:34
-	466	storage ADJ electrode with contact ADJ hole	USPAT; EPO; JPO	2001/07/03 11:24
-	1	(storage ADJ electrode with contact ADJ hole) and ((electrode ADJ pattern) and (contact ADJ hole)) and pixel) and TFT)	USPAT; EPO; JPO	2001/07/03 11:24
-	573	driving ADJ pattern	USPAT; EPO; JPO	2001/07/03 13:06
-	573	driving ADJ pattern	USPAT; EPO; JPO	2001/07/03 13:08
-	0	((electrode ADJ pattern) and (contact ADJ hole)) and pixel) and TFT) and (driving ADJ pattern)	USPAT; EPO; JPO	2001/07/03 13:08
-	0	annular ADJ (driving ADJ pattern)	USPAT; EPO; JPO	2001/07/03 13:09
-	0	peripheral ADJ (driving ADJ pattern)	USPAT; EPO; JPO	2001/07/03 13:09
-	9	((electrode ADJ pattern) and (contact ADJ hole)) and pixel) and TFT) and (peripheral or annular) ADJ driving	USPAT; EPO; JPO	2001/07/03 13:59
-	0	(peripheral or annular) ADJ driving ADJ layer	USPAT; EPO; JPO	2001/07/03 14:00

-	1	(peripheral annular) ADJ driving ADJ (layer wiring)	USPAT; EPO; JPO	2001/07/03 14:01
-	1052	source ADJ electrode with contact ADJ hole	USPAT; EPO; JPO	2001/07/03 16:36
-	389	(source ADJ electrode with contact ADJ hole) and pixel	USPAT; EPO; JPO	2001/07/03 16:37
-	144	(source ADJ electrode with contact ADJ hole) with pixel ADJ electrode	USPAT; EPO; JPO	2001/07/03 16:38
-	1	((source ADJ electrode with contact ADJ hole) with pixel ADJ electrode) and drain ADJ electrode ADJ pattern	USPAT; EPO; JPO	2001/07/03 16:39
-	34	((storage ADJ capacitance) and ((pixel ADJ electrode) and (thin ADJ film ADJ transistor or TFT))) and (electrode ADJ pattern)	USPAT; EPO; JPO	2001/07/05 11:36
-	7	storage ADJ electrode ADJ pattern with contact ADJ hole	USPAT; EPO; JPO	2001/07/05 12:09
-	466	storage ADJ electrode with contact ADJ hole	USPAT; EPO; JPO	2001/07/05 11:48
-	17	(storage ADJ electrode with contact ADJ hole) and pixel	USPAT; EPO; JPO	2001/07/05 11:48
-	0	storage ADJ electrode ADJ pattern with pixel	USPAT; EPO; JPO	2001/07/05 12:08
-	0	storage ADJ electrode ADJ pattern and pixel	USPAT; EPO; JPO	2001/07/05 12:09
-	66	storage ADJ electrode with pixel	USPAT; EPO; JPO	2001/07/05 12:10
-	1	(storage ADJ capacitance ADJ pattern) and (contact ADJ hole)	USPAT; EPO; JPO	2001/07/05 12:20
-	0	(storage ADJ capacitance ADJ pattern) with pixel	USPAT; EPO; JPO	2001/07/05 12:20
-	2	(storage ADJ capacitance ADJ pattern) and pixel	USPAT; EPO; JPO	2001/07/05 12:21